



Skill Development Program on **DIGITAL IC DESIGN USING CADENCE TOOLS** **UNDER C2S PROGRAMME**

in association with

**IEEE STUDENT BRANCH, IIC ,DEPARTMENT FORUM –SPECTRA,
ACHARYA INSTITUTE OF TECHNOLOGY, BANGALORE**



05th to 11th March 2025 | 09:00 am to 05:00 pm
VLSI Lab, ECE Department, Acharya Campus, Bangalore

About the Program

To provide insights about Digital IC Design, RTL Coding for Combinational and Sequential circuits and RTL to GDS II Design flow using Cadence tools. This program also provide to students hands-on experience with Cadence tools in order to simulate the development of cutting-edge technologies and methodologies for developing design excellence in the field of microelectronic system verification, design, and implementation.

Objectives of the Program

- To familiarize with design and functional simulation of digital circuits using Cadence Tools
- Understand RTL to GDS II design flow

Expected Outcome of the Program

- Simulate Synthesize and performance analysis of digital designs in-terms of Area, Power and Speed
- Team Building and Communication skills through project based learning and presentation

Important Dates

Event Details	Dates
Important Dates	05 th - 11 th Mar 2025
Time	09:00 AM to 05:00 PM
Target Audience	ECE Students
Type of program	Offline
Venue	VLSI Lab, ECE Department

Note :

1. No Registration Fee
2. E-Certificate will be provided upon successful completion of the assessment during the program

Committee Members

ADVISORY COMMITTEE
<ul style="list-style-type: none"> • Dr Rajeswari, Principal of AIT, Bengaluru • Dr. Marigowda C K, Vice-Principal of AIT, Bengaluru • Dr. Anil Kumar Ramesh, Head, R & D, Dover India Private Ltd., Bengaluru • Dr. S K Murthy, Patent Counsel, Intel Technologies, Bengaluru • Dr. H V Ravish Aradhya, Professor, RV college of Engineering, Bengaluru • Mr. M Venkatas, CEO, Technocarve Solutions, Bengaluru • Mr. Manjunath Srinivasaiah, Project Manager, Robert Bosch, Bengaluru
CONVENER
<ul style="list-style-type: none"> • Dr. Rajeswari , Principal and Head Dept. of ECE, AIT
COORDINATORS
<ul style="list-style-type: none"> • Mrs.Sumangala S J • Dr. Nikita Kar Choudhary

Resource Persons Details

<p>Dr. Jayalaxmi H Designation : Associate Professor, Department of ECE ,Acharya Institute of Technology</p>	<p>Dr. Nagapushpa K P Designation : Assistant Professor, Department of ECE, Acharya Institute of Technology</p>
<p>Dr. Asha C.N Designation : Associate Professor, Department of ECE ,Acharya Institute of Technology</p>	<p>Prof. Veena Sanath Kumar Designation : Assistant Professor, Department of ECE, Acharya Institute of Technology</p>
<p>Dr. Munnavar Sheriff I Designation : Assistant Professor, Department of ECE, Acharya Institute of Technology</p>	<p>Dr. Nikita Kar Choudhary Designation : Assistant Professor, Department of ECE, Acharya Institute of Technology</p>
<p>Prof. Mrs.Sumangala S J Designation : Assistant Professor, Department of ECE, Acharya Institute of Technology</p>	

Technical Team

- Mr.Vinod K R, Lab Instructor, Dept of ECE, Acharya Institute of Technology
- Mrs. Manjula S, Lab Instructor, Dept of ECE, Acharya Institute of Technology



Acharya Institute of Technology

Acharya Dr. S. Radhakrishnan Road, Acharya P.O
Soladevanahalli, Bangalore - 560107, Karnataka, India.

LOCATE US

Event Coordinator

Mrs. Sumangala S J
Department of ECE