



5 Days FDP on

“COMPREHENSIVE INSIGHTS INTO
VLSI DIGITAL AND ANALOG DESIGN”

10th to 14th February 2025 | 09:00 AM to 04:30 PM

VLSI Lab, ECE Department, AIT, Acharya Campus , Bangalore

About the Program

Objectives of the Program	The objective of the programme "Comprehensive Insights into VLSI Digital and Analog Design" is to equip participants with an in-depth understanding of the fundamental principles and advanced methodologies in VLSI design, encompassing both digital and analog domains. The programme aims to bridge the gap between theory and practical application by providing hands-on experience with industry- standard tools and techniques. It seeks to foster a deeper appreciation of the interplay between digital and analog components in modern VLSI systems, while also highlighting emerging trends and innovative approaches in the field. By encouraging collaborative learning and interaction with experts, the programme aspires to empower educators and researchers to excel in teaching, drive impactful research, and contribute to the development of cutting-edge VLSI technologies.
Expected Outcome of the Program	<ul style="list-style-type: none">Participants will gain a deeper understanding of the fundamental and advanced concepts in VLSI digital and analog design.Participants will acquire hands-on experience with modern VLSI design tools and simulation software, enhancing their practical skills.Faculty members will be better equipped to teach VLSI-related topics using innovative pedagogical strategies and real-world examples.The programme will inspire participants to pursue impactful research in VLSI design, leveraging the knowledge and skills acquired during the sessions.

Important Dates

Date	10 th - 14 th Feb 2025
Time	09:00 AM - 04:30 PM
Target Audience	Faculties of ECE , EEE , MT
Venue	VLSI Lab, ECE Department
Type of Program	Offline

Resource Person Details



Dr Rajeswari

Principal and HOD of AIT



Dr. Jayalaxmi H

Associate Prof, Dept of ECE, AIT



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Associate Prof, Dept of ECE, AIT



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Assistant Prof, Dept of ECE, AIT



Mrs. Veena Sanathkumar

Assistant Prof, Dept of ECE, AIT



Mr. Munnavar Sheriff I

Assistant Professor, Dept. Of ECE, AIT

Committee Members

ADVISORY COMMITTEE	+
<ul style="list-style-type: none">Dr Rajeswari, Principal of AIT, HOD,Dept.of ECE Dept, BengaluruMr. Marigowda C K, Vice-Principal of AIT , BengaluruDr.Rajanna ,Dean Student Affairs,AIT, Bengaluru	
CONVENER	+
<ul style="list-style-type: none">Dr Rajeswari, Principal of AIT, HOD,Dept.of ECE Dept, Bengaluru	
CO-ORDINATORS	+
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LOCATE US

Event Coordinator

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